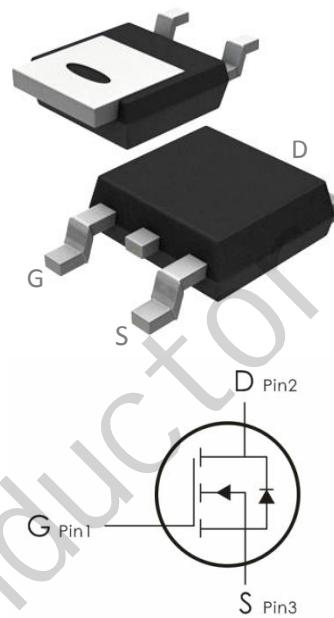


Description:

This N-Channel MOSFET uses advanced trench technology and design to provide excellent $R_{DS(on)}$ with low gate charge. It can be used in a wide variety of applications.



Features:

- 1) $V_{DS}=30V, I_D=60A, R_{DS(on)}<8.5m\Omega @V_{GS}=10V$
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra low $R_{DS(on)}$.
- 5) Excellent package for good heat dissipation.

Absolute Maximum Ratings: ($T_c=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
V_{DS}	Drain-Source Voltage	30	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current $T_c=25^\circ C$	60	A
	Continuous Drain Current $T_c=100^\circ C$	33	
E_{AS}	Single Pulse Avalanche Energy ^{note2}	36	mJ
I_{DM}	Pulsed Drain Current ^{note1}	200	A
P_D	Power Dissipation $T_c=25^\circ C$	33	W
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to +175	$^\circ C$

Thermal Characteristics:

Symbol	Parameter	Max	Units
R_{eJC}	Thermal Resistance,Junction to Case	3.8	$^\circ C/W$

Electrical Characteristics: ($T_C=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250 \mu\text{A}$	30	---	---	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=30\text{V}$	---	---	1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
On Characteristics						
$V_{\text{GS}(\text{th})}$	GATE-Source Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}, I_{\text{D}}=250 \mu\text{A}$	1	1.5	2.5	V
$R_{\text{DS}(\text{ON})}$	Drain-Source On Resistance ^{note3}	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=25\text{A}$	---	6.2	8.5	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=15\text{A}$	---	10	14	
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{\text{DS}}=15\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	---	1120	---	pF
C_{oss}	Output Capacitance		---	170	---	
C_{rss}	Reverse Transfer Capacitance		---	145	---	
Switching Characteristics						
$t_{\text{d}(\text{on})}$	Turn-On Delay Time	$V_{\text{GS}}=10\text{V}, V_{\text{DS}}=15\text{V}, I_{\text{D}}=25\text{A}, R_{\text{GEN}}=3\Omega$	---	15	---	ns
t_r	Rise Time		---	19	---	ns
$t_{\text{d}(\text{off})}$	Turn-Off Delay Time		---	35	---	ns
t_f	Fall Time		---	21	---	ns
Q_g	Total Gate Charge	$V_{\text{GS}}=10\text{V}, V_{\text{DS}}=15\text{V}, I_{\text{D}}=25\text{A}$	---	13.3	---	nC
Q_{gs}	Gate-Source Charge		---	3.1	---	nC
Q_{gd}	Gate-Drain "Miller" Charge		---	5	---	nC
Drain-Source Diode Characteristics						
I_s	Maximum Continuous Drain to Source Diode Forward Current		---	---	50	A
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current		---	---	200	A
V_{SD}	Source-Drain Diode Forward Voltage	$V_{\text{GS}}=0\text{V}, I_s=30\text{A}$	---	---	1.2	V

trr	Body Diode Reverse Recovery Time	$I_F=30A, dI/dt=100A/\mu s$	---	25	---	ns
Qrr	Body Diode Reverse Recovery Charge		---	26	----	nC

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
2. EAS condition: $T_J=25^\circ C$, $V_{DD}=15V$, $V_G=10V$, $R_G=25 \Omega$, $L=0.5mH$, $I_{AS}=12A$
3. Pulse Test: Pulse Width $\leq 300 \mu s$, Duty Cycle $\leq 0.5\%$

Typical Characteristics: ($T_A=25^\circ C$ unless otherwise noted)

Figure 1: Output Characteristics

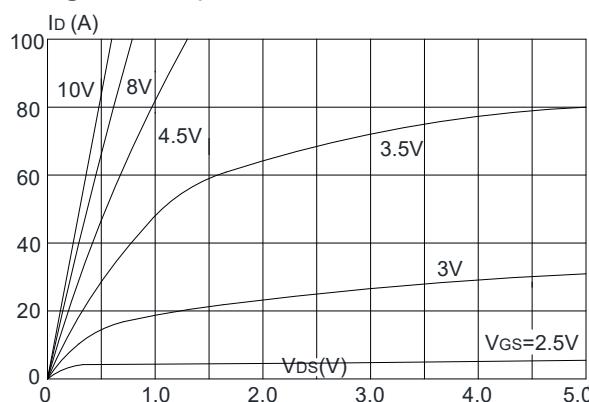


Figure 2: Typical Transfer Characteristics

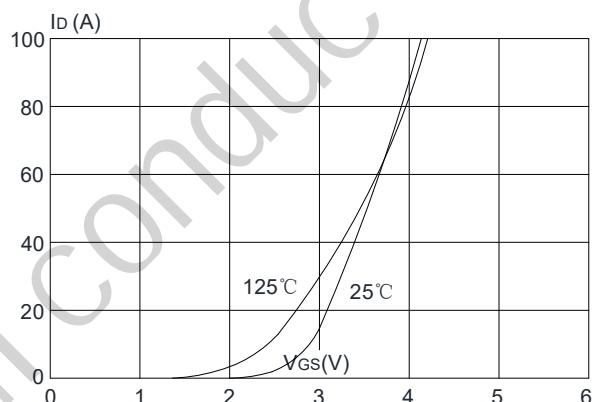


Figure 3: On-resistance vs. Drain Current

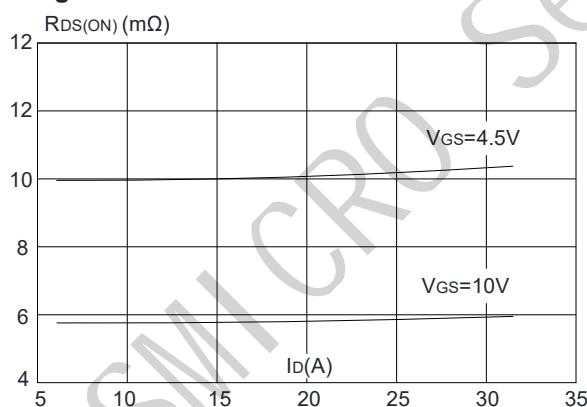


Figure 4: Body Diode Characteristics

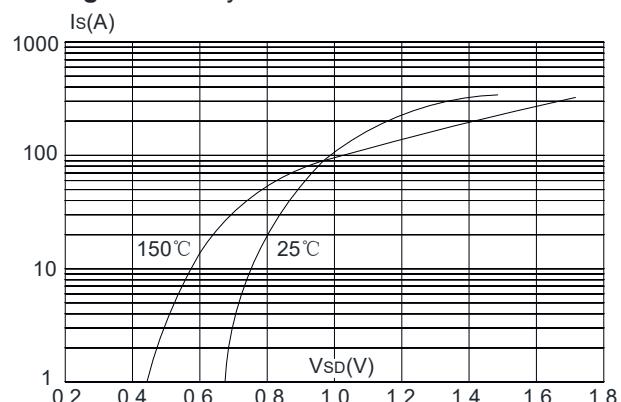


Figure 5: Gate Charge Characteristics

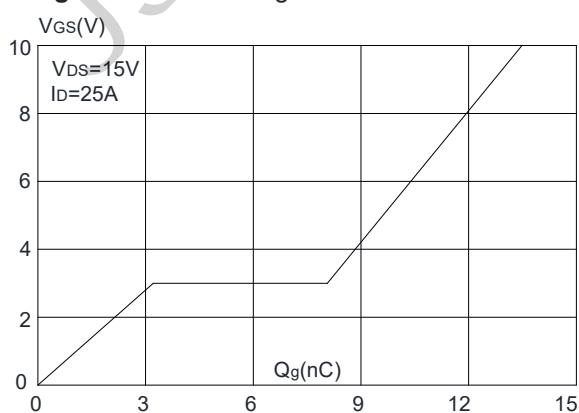


Figure 6: Capacitance Characteristics

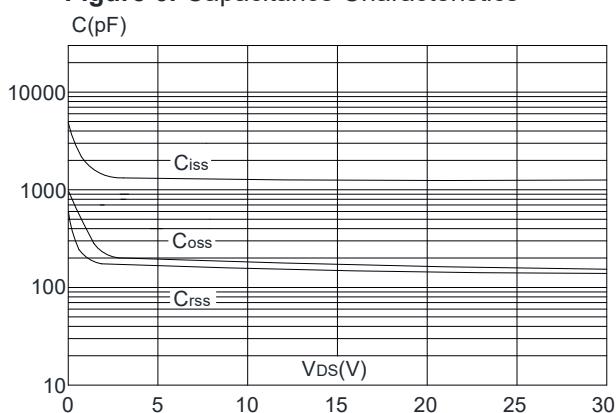


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

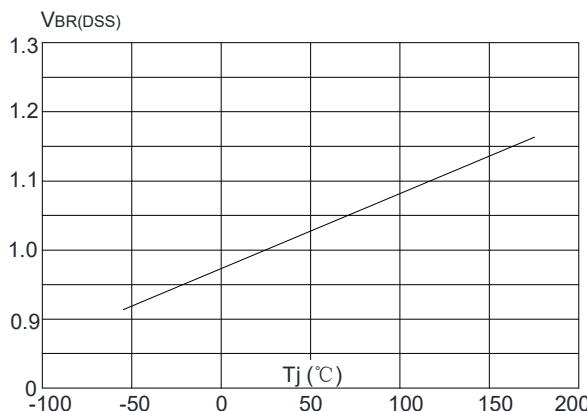


Figure 8: Normalized on Resistance vs. Junction Temperature

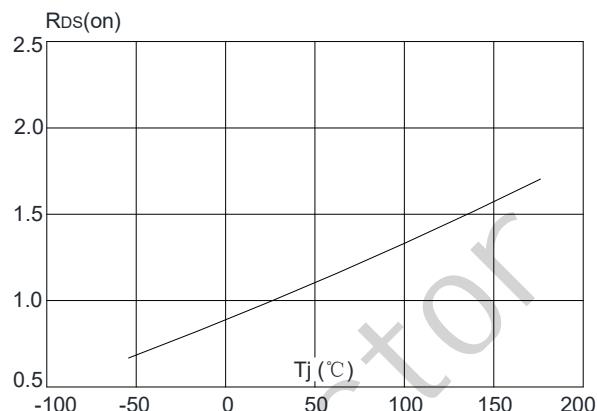


Figure 9: Maximum Safe Operating Area

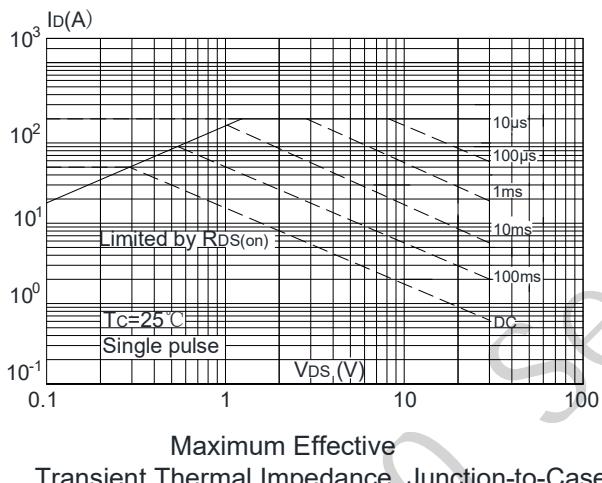
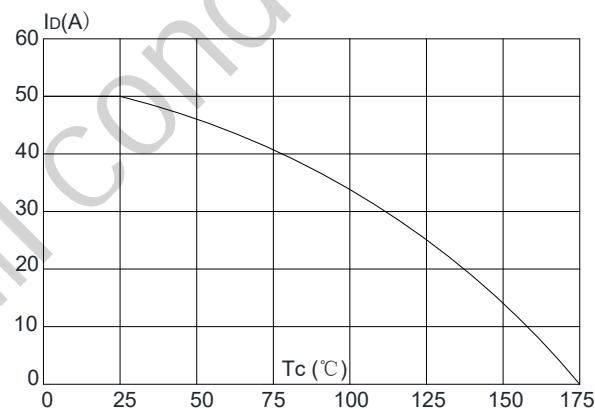
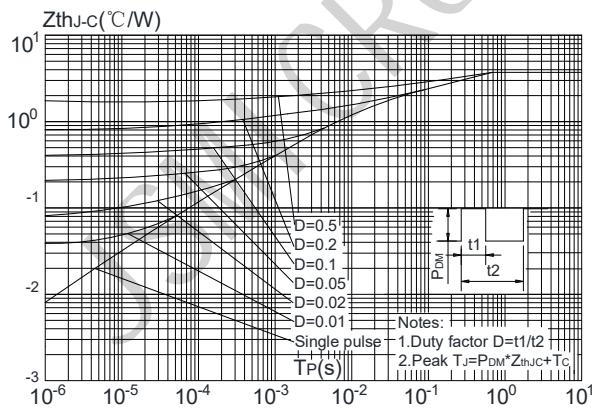


Figure 10: Maximum Continuous Drain Current vs. Case Temperature



Maximum Effective
Transient Thermal Impedance, Junction-to-Case



外形尺寸图 / Package Dimensions

